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p. 1

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FACSIMILE TRANSMISSION

DATE: May 21, 2004

TO: Office of Initial Patent Examination's Filing
Receipt Corrections
@ U.S. Patent and Trademark Office
Fax No.: (703) 746-9195

FROM: Frank Chau
@ F. Chau & Associates, LLC

NO. OF PAGES TO FOLLOW: **5**

MESSAGE:

RE: U.S. Patent Application Serial No.: 10/777,864
METHOD OF PLANARIZING AN INTERLAYER
DIELECTRIC LAYER
Group Art Unit: 2811
Our Docket No.: 8021-207 (SS-19608-US)

Dear Sir/Madam:

Attached hereto, please find a Request for a Corrected Filing Receipt in connection with the above-identified patent application.

Upon review and acceptance, please provide us with an updated Corrected Filing Receipt.

Thank you,

Frank Chau

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Thank you.

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Young-Rae Park et al.

Group Art Unit: 2811

Serial No: 10/777,864

Docket: 8021-207 (SS-19608-US)

Filed: February 12, 2004

For: **METHOD OF PLANARIZING AN INTERLAYER DIELECTRIC LAYER**

Office of Initial Patent Examination
Filing Receipt Corrections
PO Box 1450
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REQUEST FOR A CORRECTED FILING RECEIPT

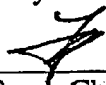
Sir:

It is respectfully requested that the Filing Receipt attached be corrected by incorporating the residence of the inventors, which is "REPUBLIC OF KOREA" as indicated in the Declaration document filed on October 14, 2003. A copy of the Declaration and a copy of the Filing Receipt which was mailed by the U.S. Patent and Trademark Office is attached with the correction indicated in red ink.

In view of this Patent Office error, no fee is due for this correction

Respectfully submitted,

By:



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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the Office of Initial Patent Examination's Filing Receipt Corrections, PO Box 1450, Alexandria VA 22313-1450 via facsimile (703-746-9195) on May 21, 2004.

Dated: 5/21/04



Frank Chau



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/777,864	02/12/2004	2811	860	8021-207 (SS-19608-US)	11	25	3

CONFIRMATION NO. 6119

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FILING RECEIPT



OC00000012578132

Date Mailed: 05/10/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

SAMSUNG ELECTRONICS CO., LTD.;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2003-48432 07/15/2003

If Required, Foreign Filing License Granted: 05/08/2004

Projected Publication Date: 01/20/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method of planarizing an interlayer dielectric layer

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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